

OPTION SHEET FOR NANOPOWER BPX

Customer Product ID: _____ (optional, enter your reference here)

Order number: _____

Example Use:

Option A	✓	= Yes
Option B		= No

1. Configuration Table

Battery Configuration	
8 cells: 2S4P	
8 cells: 4S2P	
8 cells: 8S1P	

Enabling – See chapter 2	
Option 1 – P31u (Enable Pin 10: 0 V - 3.3 V)(KS pin 14: “don’t care”)	
Option 2 – P60 - Default (Enable Pin 10: 0 V - 3.3 V)(KS pin 14: “don’t care”)	
Option 3 (Enable Pin 10 “don’t care”)(KS pin 14: 0 V – VBAT_RAW)	

Misc. options	
Full conformal coating of PCB, cell interconnects etc. (extra cost) Can only be checked if option is procured	
ISS upgrade of NanoPower products to meet NASA and NanoRacks safety requirements for ISS launch. Including flight acceptance test of NanoPower products for ISS use (extra cost)	

2. Enabling

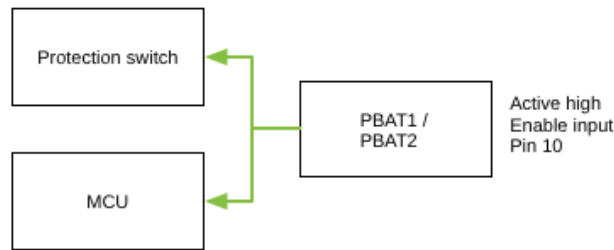
To power on the BPX an external source is needed to control pin 10 and/or 14 on PBAT1 or PBAT2.

Enable BPX (0 – 3.3 V) is active high input on pin 10 on PBAT1 and PBAT2. Pull down through 10 KΩ to ground.

Kill switch (0 V - VBAT_RAW) is active low on pin 14 on PBAT1 and PBAT2. Pulled up to VBAT_RAW through 300KΩ.

Option 1 – P31u

Pin 10 (Enable BPX) controls the protected battery power on PBAT1 (pins 1-4) and powers on all the onboard circuits. Pin 14 (kill switch) is not used.

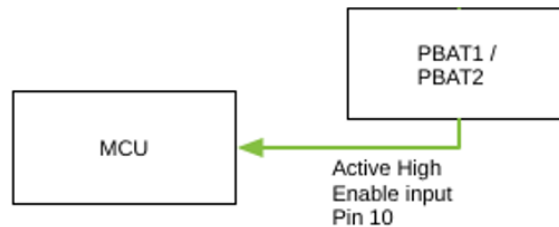


Option 2 – P60

Default configuration.

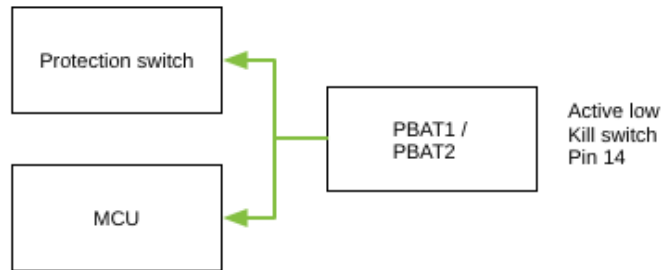
Pin 14 (kill switch) is not used.

Pin 10 (Enable BPX) only controls the MCU used for housekeeping and telemetry data over the I2C interface on PBAT1 and PBAT2 (pins 9 and 11).



Option 3

Pin 14 (Kill switch) controls the protected battery power on PBAT1 (pins 1-4) and powers on all the onboard circuits. Pin 10 (Enable BPX) is not used.



3. Disclaimer

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Author: PNN

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